

computing@tanet.edu.te.ua www.tanet.edu.te.ua/computing ISSN 1727-6209 International Scientific Journal of Computing

HART PROTOCOL ANALYSER BASED IN LABVIEW

J.M. Dias Pereira^{1,2)}, Octavian Postolache^{1,2)}, P. Silva Girão²⁾

1) Escola Superior de Tecnologia, Instituto Politécnico de Setúbal, Rua do Vale de Chaves, Estefanilha, 2910-761 Setúbal, Portugal, E-mails: joseper@est.ips.pt, poctav@alfa.ist.utl.pt

2) Instituto de Telecomunicações, DEEC, IST, Av^a Rovisco Pais, 1049-001 Lisboa, Portugal

E-mails: psgirao@alfa.ist.utl.pt, hgramos@alfa.ist.utl.pt

Abstract: Analysis of Highway Addressable Remote Transducer (HART) protocol is important in reliability evaluation of HART based systems. The present work proposes a virtual instrument based solution for HART signal analysis that includes a digital oscilloscope with GPIB interface, a HART modem, a PC with a GPIB interface board and a RS232 port, and a software component developed in LabVIEW. The HART analyser permits the visualisation of signals that correspond to the HART protocol, gives access to data coming from field instruments and has the ability to analyse and modify field instrument performance.

Keywords: Protocol analyser, instrumentation and control network signalling, intelligent instrumentation

1. INTRODUCTION

Highway addressable remote transducer (HART) systems seem to be one of the best transition solutions between the old current loop based on 4-20 instrumentation systems, mA signalling [1], and the fully digital instrumentation and control fieldbus systems [2]. HART devices can be considered a hybrid solution because they preserve the 4-20 mA signalling together with digital transmission signalling based on frequency shift keying (FSK). Two frequencies, with values equal to 1200 Hz and 2200 Hz, are used to represent digital data with a throughput equal to 1200 baud (bits per second).

The specifications of the HART protocol were established by the HCF (HART Communication Foundation) [3][4][5] and several instrumentation manufacturers offer today intelligent instrumentation devices with HART transmission capability [6][7].

This paper presents a HART protocol analyser (HPA) based on a personal computer (PC), or laptop, that can be used for centralised or field maintenance of HART instrumentation and control systems.

2. SYSTEM DESCRIPTION 2.1. HARDWARE

Figure 1 represents the block diagram of the HPA and its interconnection to the HART transmission line. The main elements of the system are: a PC or laptop equipped with a GPIB interface board (NI-PCIIA), a digital oscilloscope (Yokogawa, DL1200A) [8] and a HART interface (HI 311) [9] that provides the hardware connection between the serial communication port (RS232) and the HART transmission line.

The main features of the HI 311 include: no need of external power supply, easy installation, very low leakage current to or from the process network (lower than 10 μ A) and interconnection to the PC through a standard DB9 serial port, as represented in Fig. 2.



Fig. 1 - Block diagram of the HART protocol analyser and interconnection to the HART transmission line: PS- power supply, PSI- power supply impedance, Osc.- digital oscilloscope, PC- personal computer, HI 311- HART serial interface, GPIB- general purpose interface bus, T- terminator (250 Ω/0.25 W resistor).

Oscilloscope channels 1 and 2 are used in differential mode to acquire the FSK signalling. Channels 3 and 4 are used to capture the digital

information associated with the RS232 received signal and transmitted signals, respectively.



Fig. 2 - Interconnection between the PC and the transmission medium: WS-wave shaper, BPF- bandpass filter, H- hybrid interface circuit.

Data acquisition from the oscilloscope is made through GPIB by binary transfer from the display memory (vector with 1001 elements for each channel). The main characteristics of the oscilloscope include an input sensitivity of 2 mV/div, a vertical resolution of 8 bits and a time base accuracy of \pm 0.01% of the display time interval. These specifications are more than sufficient to obtain an accurate measurement of HART signal parameters.

2.2. SOFTWARE

The system software uses the graphical programming language LabVIEW 6.1 [10], which enables an easy and flexible software development and, if necessary, a remote TCP/IP operation by using the Internet toolbox functions library.

The main functions of the program include data communication with processing tasks. The communication with the digital oscilloscope and the HART transmission line uses the GPIB and HI 311 interfaces, respectively.

A fundamental task of the data communication software is the synchronisation of the HART signal that must consider the standardised HART frame format represented in Fig. 3.



Fig. 3 - HART frame format: Ppreamble, SC- start character, ADaddress, CN- command number, BC- byte count, S- status, D- data, CS- checksum.

Triggering of frame acquisition is provided by the preamble field of the HART frame that contains a sequence between five and twenty hexadecimal "FF" characters.

3. RESULTS

To evaluate some capabilities of the proposed HPA, a simple flow control system is considered.

The system includes a remote Coriolis effect based flow transmitter (Micromotion RFT9712), a PID controller (Onneywell UDC3000) and a pneumatic valve (Honneywell SCR $\frac{1}{2}$ "). The remote flow transmitter is a microprocessor-based mass flow transmitter that simultaneously measures flow rate, temperature, density and total flow.

Figure 4 represents the HART frame data for a flow measurement of 30 kg/minute. The message from the slave to master is of the short format type (SC=06), the flow transmitter address is equal to 86, the command

number associated with the measurement of the primary variable is 01, the status or response code is zero, which means that no communication, command or field errors are present. Finally, it is important to note that the measured data (30 kg/min.) uses the standardised IEEE 754 single-precision floating-point format, as defined by HCF.



Fig. 4 - HART frame data for a flow measurement of 30 kg/minute.

Figure 5 represents data captured from the digital oscilloscope. The graph on the left side refers to the HART signal and correspondent digital data received by the HART interface (HI 311). Amplitude and base time settings of the oscilloscope (CH1- 500mV/div, CH2- 5V/div, time/div=2 ms) allow us to measure near 750 mV for the amplitude of the FSK signal, 5 V for the amplitude of the digital received signal and a baud rate approximately equal to 1176.5 bits/s.

The graph on the right side refers to the HART signal and correspondent digital received and transmitted data. However, this graph uses a larger time scale (1s/div) in order to observe long-term variations of the HART signal. The upper curve represents the HART transmission signal that contains the 4-20 mA information superimposed to the digital FSK signal. The 4-20 mA signal variation is directly related with the flow rate measurement, whose variation was easily obtained by opening and closing a manual valve, and the FSK signal, represented by the signal bursts, contains the digital information associated with the HART commands and responses. In this case, the digital measured data contains information about the



Fig. 5 - Digital oscilloscope captured data: (a) HART and digital received signal with a time scale of 2 ms/div (b) HART and digital received and transmitted signals with a time scale of 1 s/div.

HART Analyser.vi					- D×]
File Edit Operate Tools Bro	owse <u>W</u> indow <u>H</u> elp				HART	
HART NETWORK ACTIVITY ANALYSER						-
Equipment data Manufacturer Micro Motion		Measured Variables	Current (mA)	7.638	- [
Address 1F07008B6C	TAG FT2000	SV(grdC) 23.488	PV(l/min)	2.728		
		TV(I) 1782.024	FV(kg/m3)	1006.522		
Sent data Read current and four dynamic variables Checksum 7E Error Command 03						
Received data Checksum 0000						
Communication error No. error		Status 68 Field device status device is OK				
		Error				
Statistical data	no. updates 7F64 r	o.errors 002C B.E.R.	1.30E-3	STOP		
					ر ا	1.

Fig. 6 - LabVIEW front panel of the HART network activity analyser (PV- primary variable, SV- secondary variable, TV- third variable, FV- fourth variable, B.E.R.- bit error rate).

multiple parameters processed by the HART flowmeter (RFT9712), namely: flow rate, total flow, density and temperature. From this figure it can also be verified that the delay between the command transmission (channel 3) and the response reception (channel 2) is negligible but the number of digital measurements per second (updates) is only about 0.4 s⁻¹.

Referring to the HART protocol analysis capabilities, the LabVIEW program decodes sent and received data and presents the following information (see Fig. 6 representing the front panel of the system): equipment data, measured variables values, sent data command and checksum bytes and received data checksum, error and status information. Some statistical data can also be accessed, including the bit error rate (BER), which is one of the most important transmission parameters because it gives a direct measurement of channel transmission quality.

4. CONCLUSION

The proposed protocol analyser includes, among others, the following main characteristics: capability to trace HART messages between devices; remote and local operation modes; flexible programming; capability to identify system faults and transmission overloads and delays; centralised maintenance without need of handheld communicators and userfriendly man-machine interface.

Network performance can also be evaluated using character and messages transmission errors detected by analysing parity and frame check sequence (CS) bits, respectively.

5. ACKNOWLEDGMENT

This work was supported both by Portuguese Science and Technology Foundation PRAXIS XXI program FCT/BPD/2203/99 and by Project FCT PNAT/1999/EEI/15052. We also would like to thank the Departamento de Sistemas e Informática da ESTSetúbal/IPS, for their important technical support. These supports are gratefully acknowledged.

6. REFERENCES

- [1[ISA, ANSI/ISA-S50.1-1982. Compatibility of Analog Signals for Electronic Industrial Process Instruments, Feb. 1982.
- [2] J. Berge. Introduction to Fieldbuses for Process Control. *The Instrumentation Systems, and Automation Society,* 2002.
- [3] HART communication Foundation. *Application Guide HCF LIT 34*, 1999.
- [4] HART Communication Foundation. Save Maintenance Costs with HART Loop Monitors, *HART Line*, No.2, 2000.
- [5] R. Bowder, Fisher-Rosemount. HART Field Communications Protocol – A Technical Description. *Emerson Process management*, February 2002.
- [6] Peter Devine, "RADAR Level Measurement The User's Guide", Vega Controls Lda, 2000.
- [7] Micro Motion, *Remote Flow Transmitter, Model RFT9712 – Instruction Manual*, Jan. 1991.
- [8] Yokogawa, Digital Oscilloscope Model DL1200, 1991.
- [9] Smar, Hart Serial Interface HI311, 1998.
- [10] National Instruments, *LabVIEW 6.1 User's Guide*, 2001.



J.M. Dias Pereira (M'02, S'04) received his degree in Electrical Engineering from the Instituto Superior Técnico (IST) of the Technical University of (UTL) in 1982. He Lisbon worked for eight years for Portugal Telecom in digital switching and transmission systems. In 1992. he returned to

teaching as Assistant Professor in Escola Superior de Tecnologia of Instituto Politécnico de Setúbal, where he is, at present, a Coordinator Professor. In 1995, he received the MSc degree and in 1999 the PhD degree in Electrical Engineering and Computer Science from IST. His main research interests are in the instrumentation and measurements areas.

Octavian Postolache (M'99) was born in Piatra Neamt, Romania, on July 29, 1967. He received the electrical engineering diploma from Technical University of lasi, Faculty of Electrical Engineering in 1992. In 1992, he joined the Faculty of Electrical Engineering



lasi, Department of Electrical Measurements as an Assistant Professor where he is currently Aux. Prof.. In the last two years he develop a research activity on Instituto Superior Tecnico of Lisbon. His main research interests concern on intelligent sensor, laser systems and neural processing in automated measurement systems.



P.M.B. Silva Girão (M'00, SM'01) was born in Lisbon, Portugal, on February 27, 1952. He received the Ph.D. degree in electrical engineering from the Instituto Superior Técnico of the Technical University of Lisbon (IST/UTL) in 1988. In 1975, he joined the Department of

Electrical Engineering at IST/UTL, first as an Assistant Professor and, since 1988, as an Associate Professor. His main research interests concern instrumentation, measurement techniques as well as physical and mathematical problems involved in modelling magnetic materials. Metrology, quality and electromagnetic compatibility are also areas of regular activity mainly as auditor for the Portuguese Institute for Quality (IPQ).